HONOURS				
Week	Begins	Topic	Topic	Topic
0	3 Feb	Project Management	GPU	
1	10 Feb			
2	17 Feb			
3	24 Feb	Machine Learning		
4	2 Mar			
5	9 Mar			Computer Hardware Interfacing
6	16 Mar	Security		
7	23 Mar			
	30 Mar	VAC		
	6 Apr			
8	13 Apr		Image Processing	Computer Hardware Interfacing
9	20 Apr			
10	27 Apr	Distributed & Parallel Programming		
11	4 May			
12	11 May			
13	18 May		Project Management	
	25 May	Swot Week		
		June Exams: Friday 29 May – Friday 19 June		
14	13 Jul	Architecture		
15	20 Jul			
16	27 Jul			
17	3 Aug			
18	10 Aug	Hardware Logic Design and Analysis		
19	17 Aug			
	24 Aug			
20	31 Aug	Project time		
21	7 Sep			
22	14 Sep			
23	21 Sep			
24	28 Sep			
25	5 Oct			
26	12 Oct			
	19 Oct	Swot Week		
		November Exams: Friday 23 October – Friday 20 November		

Honours

The Honours Degree will benefit your career because:

- The Honours level is the international standard for a first degree. For international mobility, and entrance into postgraduate courses at foreign universities, the honours degree is a minimum requirement.
- The Rhodes degree is an advanced, marketable qualification, recognised internationally. The programme provides improved employment prospects (not only now, but later on in your career), and opens up increased opportunities for entrepreneurial prospects.
- It is the degree that provides entry into research and development.
- It allows specialisation not possible in general undergraduate degrees, and satisfies love of the subject.

COURSE CO-ORDINATOR

Your course co-ordinator is **Dr Dane Brown**, room 106, Hamilton Building (D.Brown@ru.ac.za).
Queries and comments about the Honours course should be addressed to the course co-ordinator.

ADMISSION CRITERIA

While admission to the Honours programme requires a minimum mark of 60% for CS3 (aggregate for CSc301 and CSc302), students intending to study Honours should aim considerably higher than this minimum, and obtaining the required minimum does not automatically imply acceptance into Honours.

Acceptance into any postgraduate course is at the discretion of the Department. A number of factors may be considered when we make this decision. Space or supervision constraints may limit the number of students we can accept. We will also consider performance and participation across the whole of your academic career. We may also seek evidence of passion and participation in the discipline outside the confines of our courses - a "portfolio". This could include software or games that you've written, courses you may have offered to others (e.g. a student society), web sites you might have built, learning a different computer language or operating system, using your Computer Science in your other subjects, software consulting, maintaining networks at a local school or business, and any software-related work during vacations.

COURSE OPTIONS

In the BSc (Honours) programme, you are required to take course options which count towards the final mark and which are examined formally in June or November. No more than 3 courses may be taken in the 2nd semester. The remaining marks come from project and assignment work undertaken during the year. Students may elect to do no more than one extra course.

For students registered for 100%:

60%	Six formal course examinations and practical work
40%	Project assessment mark made up as follows:
5%	Project related assignments through the first semester
35%	Final project report, seminar and short paper

For students registered for 60%:

20%	Two formal course examinations and practical work
40%	Project assessment mark made up as follows:
5%	Project related assignments through the first semester
35%	Final project report, seminar and short paper

For students registered for 40%:

40%	Four formal course examinations and practical work

Criteria for the marking of the project will be given in a formal Project Management course, a **compulsory** module at the start of the year, that counts towards project related assignments. This module covers various aspects involved in tackling a large project, including writing and research skills. An important component of modern science is the ability to write up one's work in a clear form, perhaps with the intention of publishing it.

Each option will have related portfolio work that will consist of at least one formal submission per course. Shaded areas in the provisional course time-table denote periods that are intended for use to mop up assignment work and to focus your energies on your project work. Some of these weeks go into the vacations. It is assumed that Honours students will be working on project coursework through the vacations.

Full time Computer Science students are required to complete six coursework modules. One of these may be an external module, with the permission of the heads of both departments concerned, with the remainder coming from the Department or Ancillary pools. Joint Honours students must complete all their modules from the Department or Ancillary pools.

This year, the following course options will be offered. They are classified into three categories.

DEPARTMENT MODULES	ANCILLARY MODULES	EXTERNAL MODULES
Architecture Computer Security Distributed & Parallel Programming GPU Programming Hardware Logic Design and Analysis Image Processing Machine Learning	Hardware Interfacing	Information Systems Electronics Mathematics Physics

Students must discuss their course configuration with their project supervisor. The project supervisor will determine whether their course configuration supports their project both in terms of time management and course content.

Students must email their course choices to the Admin Manager, and to the project supervisor, indicating that confirmation has been obtained.

EVALUATION

Please note that individual modules may have specific DP requirements that must be met before a student may write the examination for that module. Students should check with the lecturer of each module what the requirements are for that module.

- Normally, students must pass ALL qualifying coursework modules, as well as the project.
- Students should note that the marks for modules may be subject to scaling at the Department's and/or the external examiner's discretion.
- In the case of students who are not performing adequately during the year, they will be advised to withdraw from the Honours programme or to take the Honours programme over two years.

LECTURES AND TIME ALLOCATION

Lectures are either single or double period slots, which dovetail with the times of the undergraduate time-table. Other Departments use different lecturing schemes, and time tabling joint honours is sometimes rather awkward. Students planning to take modules from other Departments should be careful that their load does not become excessive at any one stage of the year.

Honours lectures will be held in the CORAL SEMINAR ROOM, HAMILTON BUILDING. You will be informed of lecture time slots by each individual lecturer.

Additional short courses of either an optional or a compulsory nature may be mounted during the year, depending on the availability of expert lecturers.

NOTE:

(a) You will also need to timetable in
  - any part time work which you may be doing
  - Departmental seminars, at which **all** postgraduate students should be present.
  - formal project supervision sessions each week, or as arranged with your supervisor.

(b) Note that there is an extended period of time in term 4 with no modules on offer. This has been intentionally done to make sure significant headway is achieved towards finalising the project, and is not meant as a vacation. Furthermore, a first full draft of the thesis will be expected at the end of this time period. In addition, enrichment lectures will be scheduled during this time to provide assistance with approaching your project and perfecting the write up. These lectures and any associated assignments are **compulsory.**

(c) Attendance at lectures, practicals, seminars and project supervision sessions is compulsory.

(d) To give you an idea of how we pitch the course work, we expect honours students to put in 50 hours of work per week. When you are attending two course options, the approximate breakdown should be:
  - 10 hours on lectures
  - 2 x 10 hours on practicals
  - 20 hours on your project

When you only attend one course option, and during break periods, you are expected to put more time into your project work.

HONOURS PROJECTS

A very important part of the Honours year is the project, which is undertaken on a rather grander scale than anything you might have done in your undergraduate years. At one stage we adopted a policy whereby students could identify their own project, if that seemed possible, but this led to some problems in supervision and motivation, and we now favour a scheme where we provide a fairly small list of projects that are of immediate relevance to staff and research interests here.

Ideally, we would like each student to be part of a team working in an area related to one of the main research thrusts in the Department, primarily the Convergence group, the Parallel and Distributed group, the Security group or the Distributed Audio group (see Research section). You are advised to chat to staff members involved with a particular project you might be interested in before making a final choice. Project choices should be finalised by the second week of lectures. While there are different research groups and specialisations, Honours is your introduction to research. As such, the specific project you choose or area of research you go into at this early stage is not crucial, neither does it disqualify you from changing direction later and pursuing an area that diverges from your project. The purpose is to guide you and equip you with an academic approach to research, and this is independent of the project you choose.

An important part – perhaps the most important part – of the effort in the project goes into producing the report. Each report should be produced in the style of a paper for publication. We also require you to produce a short paper of your project work. One of the supervision requirements of all postgraduate projects is that the student set up and maintain a web page for their project, under the main research site of the Department and this must be updated regularly. Requirements for the project web page appear later in this handbook.

The final project reports have to include machine readable versions of the report as well as of any software written.

HANDOUT FEE

A non-refundable charge of R300-00 will be made to cover the cost of course handouts, laboratory consumables, and payments of royalties on copyright material. We keep this charge as low as possible. This amount will be debited to your student account.

AND FINALLY ...

Honours can be very worthwhile – but, like much else in life, it depends largely on what you put into it. We often have Honours students remarking that they have learned more about Computer Science in their Honours year than in their whole undergraduate degree. We hope, and expect, that you will put a lot into it, starting from day one. At this level everyone in the course – staff and students alike – become contributors to a pool of knowledge. Your project work, and the seminar programme, for example, become important areas in which information can be spread about the group as a whole.

A prize, generously sponsored by
Open Box Software,
is offered for the top student in the course

and a prize, generously sponsored by
Jannine Franke,
is offered for the top Honours research project.

SUMMARY OF HONOURS COURSES

All students who have not completed their undergraduate degree through Rhodes University are advised to discuss their module choices, in terms of what knowledge is assumed, with the relevant module lecturers at the beginning of the year.

Research (Project) Management Course (Prof Philip Machanick)

Objectives:
This course aims to equip you with the skills to complete a fairly substantial research project.

Contents:
The entire life cycle of the project is covered in detail -- from inception (finding a project topic and writing the proposal) to completion (thesis structure and assessment). Other topics cover the purpose of, and how to write a literature survey, how to present a research seminar and general issues like research ethics, time management, scientific writing skills, and the use of LaTeX and BibTeX.

This module is compulsory, is assessed throughout the year and examined in the final project report.

Architecture (Prof Philip Machanick)

This course covers fundamentals of instruction set design, memory architecture and how the various components interact in achieving performance goals – whether speed, cost or energy. Practical components focus on how the RISC-V architecture has aimed to improve on previous designs. We will use simulations to study performance impacts of design decisions.

The aim of the course is to leave you with an appreciation of how to make a suitable choice among existing designs as well of research in computer architecture.

Contents:
- Instruction set architectures and design principles
- Layers of architecture
- Modes of parallelism
- Memory hierarchy

Prerequisites: introductory computer architecture, C or C++ programming in a Unix environment.

Computer Security (Prof Barry Irwin)

Objectives:
To enable students to gain a better understanding of the importance of Security within the larger realm of Information Technology and to provide insight into the effects of security on Information Systems. Emphasis will be placed on the importance of holistic integration of security practices into Information Technology. The course curriculum is structured around the internationally recognised Certified Information Systems Security Professional (CISSP) certification.

Contents:
The following components relating to Information Security are explored:
- Cryptography
- Web Application Security
- Operations Security
- Security Management
- Physical Security
- Disaster Recover and Business Continuity Planning
- Network and Telecommunications Security
- Incident Response
- Threat Intelligence

Prerequisites: Students should be familiar with programming constructs such as those covered in CSc201/CSc202 and a good understanding of Computer Networks and Operating Systems as covered in CSc302 and CSc202 respectively.

Distributed & Parallel Programming (Prof George Wells)

Objectives:
To present students with an overview of distributed computing, parallel programming, and the relationship between them. Practical sessions cover the full range of parallel and distributed processing, and use the Java programming language, which is very similar to C#.

Contents:
Background, Terminology and Theoretical Foundations
- Hardware issues and system architectures
- Parallel algorithm patterns

Parallel Processing
- Communication and synchronization
- Threads, Interprocess communication, CSP, etc.

Distributed Processing
- Remote Procedure/Method Calls, Virtual Shared Memory, Message Oriented Middleware, Grid Computing, etc.

Formal specification
- The CSP meta-language, specifying parallel systems, proving safety, etc.

Prerequisites: Object-oriented programming in Java (or a similar language, such as C#).

GPU Programming (Prof Karen Bradshaw)

Although graphics processing units (GPUs) are well known for their use in rendering images, their power for general parallel computing has only been explored in the past few years. With the increased availability of parallel frameworks, programming models, and development tools, however, GPUs have developed into flexible processors that typically outperform CPUs in the parallel computation of many problems.

This course covers the fundamentals of parallel computing using both OpenACC and the CUDA parallel programming model. OpenACC pragmas are introduced first as these enable acceleration of sequential code with only minor modifications, on both GPUs and multi-core CPUs.

The remainder of the course focuses on basic CUDA commands and syntax, the use of CUDA libraries, and relevant optimizations specific to the architecture of the GPUs being used. Practical labs focusing on applications in graphics, simulations, physics, and other topics complement the programming concepts and techniques introduced in the lectures.

Prerequisites: Competence in C/C++ programming and an independent work ethic are vital as the module is based on multiple hands-on assignments to understand the concepts.

Hardware Logic Design and Analysis (Dr Alan Herbert)

Objectives:

To introduce the fundamental concepts of logic design in silicon; the very foundation of modern computing. The subject matter of this course starts with an introduction to logic level design through creation of asynchronous circuits (circuits that do not rely on a clock). Following this, synchronous circuits are used to create pipelines in which more complex tasks can be achieved. Simulation of these logic circuits will be implemented on FPGA devices. This course is structured around the RTL design method, and makes use of VHDL as its primary language.

Contents:
- Asynchronous logic design
- Synchronous logic design
- Introduction to a Hardware Description Language (HDL)
- FPGA programming
- RTL design method

Prerequisites: This course requires understanding of logic, and thus at least Maths 1 or equivalent, is required. This course is focused on the design of logic circuits and not the electronics that go with it. Because of this, Physics 1, or equivalent, is not required. However, it will help with understanding the content matter.

Image Processing (Mr James Connan)

Objectives:
To introduce students to image processing. This module covers aspects of image generation and manipulation, but focuses on the extraction of information from images. The concepts covered are relevant to computer vision and image manipulation.

Contents:
Topics covered include:
- Basic concepts such as image file formats, scaling and rotation.
- Colour manipulation such as grey scaling and colour models.
- Image segmentation using background subtraction, edge detection, filters, etc.
- Image processing techniques such as Hierarchical Chamfer Distance Transforms, AdaBoost, Histograms and CAMShift.
- Taking a look at current developments in the field and directions for possible research.

Prerequisites: Enthusiasm and the ability to think outside of the box. Any language that has a wrapper for OpenCV should be usable.

Machine Learning (Mr James Connan / Dr Dane Brown)

Objectives:
To introduce students to machine learning. This module covers aspects of machine learning and their application, particularly to image processing. The concepts covered are relevant to machine learning.

Contents:
Topics covered include:
- Regression.
- Neural networks.
- Support Vector Machines.
- Hidden Markov Models.
- Clustering.

Prerequisites: Enthusiasm and the ability to think outside of the box. Strong competency in at least one programming language.

Computer Hardware Interfacing (Mr Anthony Sullivan)

This is an electronics orientated course, and is presented in the Department of Physics and Electronics.

Objectives:
To provide the student with a hands on knowledge of hardware interfacing using a microcontroller environment.

Contents:
Assembly level programming of the Atmel 8-bit RISC architecture and associated common peripheral interfaces. A strong emphasis is placed on the differences to 'normal' computer programming such as no scheduling or other OS provided crutches. Practical exposure to system design of fundamental hardware interfaces.

Prerequisites: Physics 1E2 or equivalent electronics experience.
PLEASE NOTE THERE IS A 10 PERSON LIMIT ON THIS MODULE.

The course is taught via the investigation of peripherals available on the development board, how they can be made to interact and produce a desired outcome. Short tasks will be set for investigation after each lecture (not all of these are for assessment purposes). The main assessment (besides the theory examination) is a practical assignment that effectively combines elements from each of the tasks to work together to produce a stated goal.

Masters & Doctorates

The Department of Computer Science has an established and highly respected research school. Research is funded by the Telkom Centre of Excellence in Distributed Multimedia, the National Research Foundation and Rhodes University. Each year, staff members and senior students present their work in national and international forums. Several staff members serve on international advisory boards, standardisation forums, and organising committees.

POSTGRADUATE SCHOOL LEADER

The postgraduate co-ordinator is **Prof Philip Machanick,** Room 101, Hamilton Building (P.Machanick@ru.ac.za).

Queries and comments about the degrees should be addressed, initially, to him.

MASTER OF SCIENCE DEGREES - MSC

The Department of Computer Science offers a pure research MSc degree, for which the entire assessment is by thesis, and an MSc (Applied Computer Science) degree, by course work and short thesis. Since 2011, an MSc specializing in Information Security has been activated, offering an excellent possibility of further training to practitioners in the industry. Details about this offering can be found elsewhere in this handbook.

Students work on an approved research topic, under the supervision of an experienced researcher, usually contributing towards an established project. Some of the projects offered for this purpose are undertaken in conjunction with external corporations or institutions. These projects are intended to increase the relevance to industry of the degree and usually require additional visits to the institution concerned. The research is written up as a thesis, which is examined by selected area specialists external to the University.

Duration

The residence period for the Master's degree is a minimum of one full year, from February to January of the following year. An MSc thesis typically takes between 18 to 24 months to complete.

Entrance requirements

The normal entrance requirement for an MSc candidate is an appropriate Honours degree, or an equivalent qualification. In exceptional cases a BSc degree and a minimum of three years of relevant experience may be considered for acceptance.

MASTERS DEGREE PROGRESS MILESTONES

These progress milestones are intended to indicate the maximum time that a full-time student should take on each phase of the masters degree (by thesis) in order to successfully complete in a time period not exceeding two years. This allows ample time for examining, corrections, and the submission of a final paper on the work.

Students must produce reports at the end of each quarter of study detailing their progress and the meeting of the milestone requirements. More regular interim progress notes are also encouraged. Requirements for the project web page appear later in this handbook.

Year one - quarter one

At the end of this quarter, students should have:

- written a project proposal (approximately ten pages long)
- presented a seminar on the material to his or her supervisors and peers
- created a web page for the project, containing at least an abstract and description of the project,
- links to related sites, and a research plan for the remainder of the project.

Year one - quarter two

At the end of this quarter, students should have:

- made some progress in implementing the project
- written an extended abstract for a suitable conference's "work in progress" track, outlining the project proposal.

Year one - quarter three

At the end of this quarter, students should have:
- completed a good portion of the implementation of the project and the research should be well underway
- made contact with researchers at other institutions active in the field and received wider feedback
- on the content and approach of the research project
- presented a written and oral paper on the project to a wider audience than his/her immediate institution.

Year one - quarter four

At the end of this quarter, students should have:
- completed the broad implementation goals of the project
- prepared a demonstration of the work
- submitted a revised plan of action with an implementation schedule for the 2nd year of the project and an updated literature survey
- added considerably to the number of web links to related sites
- prepared a content outline for the written thesis.

Year two - quarter one

At the end of this quarter, students should have:
- made significant progress in writing the thesis
- submitted a rough draft of the thesis to the supervisor for comment
- identified all remaining problem areas that fall within the scope of the study.

Year two - quarter two

At the end of this quarter, students should have:
- submitted a complete draft of the thesis to the supervisor for comment
- made a critical self assessment on the approach of the project and the conclusions drawn
- submitted a paper to a suitable conference outlining the project's aims, results and conclusions.

Year two - quarter three

At the end of this quarter, students should have:
- submitted the final thesis in polished form for examination
- published the detailed conference paper on their project web page
- identified a journal, with a view to publishing an enhanced version of the paper
- polished the demonstration system if necessary
- if appropriate, prepared an online demonstration of the their work into their project web page.

Year two - quarter four

At the end of this quarter, students should have:
- completed any updates or corrections required by the examiners, and submitted a final corrected thesis
- presented a final demonstration and verbal report to industrial partners/funders
- submitted a paper to a journal for refereeing, taking into account the feedback of the external examiners.

DOCTOR OF PHILOSOPHY DEGREES - PHD

As for the pure research MSc degree, the PhD degree involves the preparation of a thesis based on original and independent research. The residence period for full time pursuit of a PhD degree is normally 3 years.

A PhD candidate is expected to publish substantially more than an MSc candidate.

Research

CENTRE OF EXCELLENCE

Since 1997, the Department of Computer Science has hosted a Telkom Centre of Excellence, which focuses on distributed multimedia. The Centre brings together the research expertise within the department, contributions from other departments at Rhodes University and at other tertiary institutions (both nationally and internationally), and input from industry partners. The Centre is a good example of triple helix at work, where academia, industry and government come together to pool resources and improve the competiveness of the industry, via the preparation of highly skilled practitioners and the co-development of appropriate technology. The Centre is supported by Telkom - the anchor partner, Coriant, and the National Research Foundation (NRF).

The Centre operates under the management of a joint academic/industry steering committee, and has high level representation from the partner industries and from the DTI through the National Research Foundation. The Centre is headed by Prof Alfredo Terzoli.

RESEARCH GROUPS

The Department has a number of research and development groups, several of which feed into the realisation of the distributed multimedia platform under the auspices of the Centre of Excellence. They are:

- Bioinformatics (Prof Philip Machanick)
- Computer Vision (Mr James Connan and Dr Dane Brown)
- Convergence (Prof Alfredo Terzoli)
- Distributed and Parallel Computing (Prof Karen Bradshaw & Prof George Wells)
- Distributed Audio (Prof Richard Foss)
- ICT4D (Prof Alfredo Terzoli)
- Intelligent Robots (Prof Karen Bradshaw)
- Security and Networks (Prof Barry Irwin)
- Software Design and Development (Dr Yusuf Motara)

They are described in some detail in the next few sections.

Bioinformatics

Prof Philip Machanick researches in bioinformatics and works with academics at other universities on identifying biologically interesting research problems. In this field it is possible at PhD level to obtain a degree in either the field of Computer Science or Bioinformatics.

Computer Vision

The Computer Vision group focusses on Image Processing and Machine Learning, and the application of Machine Learning to Image Processing. As has become the norm, there is a continued shift to parallel image processing using GPUs. Emphasis is placed on the application of computer vision techniques to real world problems such as biometrics, gesture recognition, surveillance, face recognition, facial expression recognition and object tracking. Members have extensive experience in the application of computer vision to real world scenarios as former participants in the Integration of Signed and Verbal Communication: South African Sign Language Recognition, Animation and Translation Group.

Convergence

This group carries the core activity of the Centre of Excellence, working at the provision of specific support for mobile devices in the previously developed distributed media-service platform. This is done through the investigation of available architectures to specialize appropriately, and the creation of suitable toolkits for the fast creation and deployment of services on the platform. Naturally, particular attention is given to services that integrate audio, video and location information.

Distributed and Parallel Computing

This group has a long history in the department, going back to the origins of the paradigm in the 1980's when transputers were the main focus. More recently, the group's research efforts have focused on various aspects of Linda, a coordination language for parallel/distributed programming, grid computing and general purpose GPU programming. Emphasis is also placed on making concurrent programming more readily accessible to non-computer scientists.

Distributed Audio

Over the past 5 years, the Distributed Audio group has been involved in the conceptualization and implementation of a connection management and control protocol known as XFN (Cross Fire Network). This protocol is aimed at allowing for comprehensive control over the routing of audio within large sound installations such as stadiums, studios, convention centres, law courts, and live concerts. The project has involved embedded firmware programming of nodes for amplifiers and other professional audio devices, the construction and programming of special purpose routers, and the creation of graphical control software.

ICT4D

Recently, the Centre has started a large, long-term multidisciplinary effort focused on the introduction of ICT in a deep rural area in the Mbashe area, on the Wild Coast in Transkei, in collaboration with the University of Fort Hare. There, technological solutions developed by both Universities are tested in the field, in rather harsh conditions. This research effort, connected closely to the other research pursuits in the department, situates itself in the growing area of ICT for Development. The field work is being structured as a living lab (Siyakhula Living Lab), a vehicle which permits the use of a new research methodology, whereby the services needed by a community are co-created with the community.

Intelligent Robots

Intelligent robots are those that are able to mimic human behaviour in some way. Using Artificial Intelligence techniques it is possible to train a robot to perform a human task. Unsupervised deep learning can be applied to both land and aerial robots allowing these to function in a wide variety of fields.

Security and Networks (SNRG)

The Security and Networks Research Group (SNRG) efforts are concentrated in the areas of Information Security and Computer Networks. Members of the group are currently involved in a variety of projects, including a selection of visualisation techniques for security metrics, network traffic classification and monitoring and intrusion detection. Efforts are focused around the emerging field of collaborative Cyber Threat Intelligence.

Software Design and Development

The Software Design and Development group focuses on finding ways in which the next generation of software can be conceptualised, designed, and implemented. It seeks to understand the mechanics of software creation, from the fundamental philosophies that underlie software modeling up to the effect of applying language features to particular problems. Since new methods are notoriously difficult to communicate, the group is also particularly interested in ways of explaining such methods in an intuitive and simple way. Its research agenda therefore encompasses Computer Science education and gamification in this context. At present, the efforts of the group are focused in the area of functional programming as an up-and-coming area of practical importance to many aspects of software design and development.